

## Notice of References Cited

Application/Control No.

09/695,756

Applicant(s)/Patent Under

Reexamination

VOSHEL

Examiner

Guy J. Lamarre, P.E.

Art Unit

2133

Page 1 of 1

### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,134,681	10-2000	Akamatsu et al.	714/710
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Park et al. 'Address compression through base register caching, 'Microprogramming and Microarchitecture; 27-29 Nov. 1990; On page(s): 193 – 199; IEEE.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.